



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in light of NEP-2020**  
**Diploma in Electronics**

**SEMESTER- III (2024-2027)**

SEMESTER- III (2024-2027)												
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	ML307	Environmental Management and Sustainability	4	0	0	4	60	20	20	0	0	100
2	DTMT302	Basic Digital Electronics	3	0	2	4	60	20	20	30	20	150
3	DTET301	Signals and Systems	3	0	0	3	60	20	20	0	0	100
4	DTEI307N	Network Analysis	3	0	2	4	60	20	20	30	20	150
5	DTET305	PCB Design Lab	0	0	4	2	0	0	0	30	20	50
6	-	Generic Elective I	4	0	0	4	60	20	20	0	0	100
TOTAL			17	0	8	21	300	100	100	90	60	650

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

*[Signature]*

*[Signature]*

Chairperson  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*[Signature]*

Chairperson  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*[Signature]*  
Controller of Examinations  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*[Signature]*

Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

*[Signature]*

Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore